

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Naoto HORIGUCHI

Serial Number: Not Yet Assigned

Filed: March 23, 2004

For: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Attorney Docket No. 042261

Customer No.: 38834

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

March 23, 2004

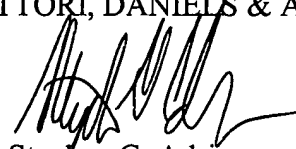
Sir:

In compliance with 37 CFR 1.56, Applicant(s) call(s) to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

Copies of the references are enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 50-2866.

Respectfully submitted,
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Enclosures: PTO-1449; Reference (2)
SGA/ll

INFORMATION DISCLOSURE STATEMENT PTO-1449	Attv. Docket No. 042261	Serial No. New Appln.
	Applicant(s): Naoto HORIGUCHI	
	Filing Date: March 23, 2004	Group Art Unit:

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA					
_____	AB					
_____	AC					
_____	AD					

FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Translation (Yes or No)
_____ AE 2003-031798	01/31/03	Japan	Abstract & Discussed in the Spec.
_____ AF 6-224381	08/12/94	Japan	Abstract & Discussed in the Spec.
_____ AG			
_____ AH			
_____ AI			

OTHER DOCUMENTS

_____	AJ			
_____	AK			
<table border="0" style="width: 100%;"> <tr> <td style="width: 50%;">Examiner</td> <td style="width: 50%;">Date Considered</td> </tr> </table>			Examiner	Date Considered
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